



Silicon Motion, Inc.

SM32X Test Program and ISP Release Note

**SM323&321 Test Program and ISP Release Note:**

| Release Date | ISP Version | ISP Check Sum | Test AP Version | Description |
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| 2004/08/15 | AB:2004-08-15 | AB:0005A0DF | 2004/08/15 | TestAP: modify setting password and Win2k,WinXP time out set to 5 minutes ISP: Fixed the MSI mother board boot problem. |
| 2004/08/23 | | | 2004/08/23 | TestAP: modify setting password to "320" |
| 2004/09/03 | AC:2004-09-02 | AC:0002AABB | 2004/09/03 | TestAP: support 323AC ISP:323AC ISP release |
| 2004/09/13 | AC:2004-09-10 | AC:0003730E | 2004/09/10 | TestAP: If enlarge bad block , format build partition table will large than real capacity ISP:fix suspend current Fixed interleave mode copy error |
| 2004/09/20 | | | 2004/09/20 | TestAP: Win98 Pretest Flash Manager Fail Win98 driver smi320.sys update to 1.2 Enhance Write Performance |
| 2004/09/24 | | | 2004/09/24 | TestAP:323AC If already download ISP , Win98 after Pretest no reset |
| 2004/10/01 | AC:2004-10-01 | AC:00051532 | 2004/10/01 | TestAP: Win98 VIA SIS Pretest Fail ISP: Micro Flash support Customer 8 LED option |
| 2004/10/14 | AC:2004-10-14 | AC:0005408E | 2004/10/14 | TestAP:add customer = 1 Led setting ISP: Fix Write Protect Page |
| 2004/10/27 | AC:2004-10-21 AD:2004-10-20 | AC:0006565C AD:0001C09A | 2004/10/27 | TestAP: support ad version ISP: Interleave +2K/P mode Flash , Micron Flash , Win2K performance . |
| 2004/10/28 | | | 2004/10/28 | TestAP:open AD autorun support |
| 2004/11/12 | | | 2004/11/12 | TestAP: 2004/10/28 version format will hang , fix the problem |
| 2004/11/23 | | | 2004/11/23 | TestAP:fix AD after test OK LED no flash |



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| 2004/11/26 | | | 2004/11/26 | TestAP : support multi device test |
| 2004/12/09 | AD:2004-12-10 | AD:00028F46 | 2004/12/09 | TestAP,ISP : fix 323AD customer=8 LED bug |
| 2004/12/13 | | | 2004/12/13 | Add Pretest time "no erase" function , for 323AD |
| 2004/12/17 | | | 2004/12/17 | Fix 12/13 bad block number is default value , can't adjust , |
| 2004/12/24 | 321AB: 2004-12-02 | 321AB:00007BCD | 2004/12/24 | Add 321 support Fix 323AD,321AB Pretest "no erase" bad block increase bug . |
| 2004/12/27 | 323AD: 2004-12-27 | 323AD:0003786A | 2004/12/27 | TestAP: open auto run make 323ADISP:fix Win98,ME auto run hang |
| 2004/12/30 | | | 2004/12/30 | TestAP: If select make auto run can't format , Fix win98 , WinXP is OS limit |
| 2005/01/27 | 321AB: 2005-01-27 | 321AB:0008612 | E0127 | TestAP: multi-device after show capacity ISP : fix 321AB customer=8 LED bug |
| 2005/01/27 | | | E0127 V2 | TestAP: Add 321AC support |
| 2005/02/02 | | | E0202 | TestAP:Add 321AC Support 0xD3,0xD5 Device ID |
| 2005/02/03 | 321AC: 2005-02-03 | 321AC:0007D5D | E0202 | ISP: 321AC ISP fix memorybar plug-in login to security |
| 2005/02/16 | | | E0216 | TestAP: 1.Pretest reference flash bad block 2.Auto Clock |
| 2005/02/17 | 323AD: 2005-02-17 | 323AD:0003ABCE | | ISP:323AD ISP fix Win2k autorun |
| 2005/02/18 | | | E0218 | TestAP: Fix 321AC test result LED no flash (for win98) 321AC K9G4G format problem |
| 2005/02/24 | | | E0224 | TestAP:321AB 0x185 set to 0 |



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| 2005/03/03 | | | E0304 | TestAP: modify customer=8 led frequency |
| 2005/03/08 | | | E0308 | TestAP: disable cache program for HT/Hynix flash Fix 321AC pretest "No erase" but bad block number reduce problem |
| 2005/03/08 V2 | | | E0308 V2 | TestAP : enable cache program for HT/Hynix flash |
| 2005/03/15 | 323AD: 2005-03-15 | 323ADISP :0003AD79 | | 323ADISP: Fix Mac OS 10.3.6 ZIP type show error message |
| 2005/03/23 | 321AC: 2005-03-03 | 321ACISP:0001E576 | E0323 V2 | TestAP: option 0x185 set to 0 , detect AGAND/MLC flash set min bad block to 44 ST/Hynix clock set to 2 321AC ISP : Fix Winxp,Win2k AutoRun |
| 2005/03/29 | 323AD_SEQ: 2005-03-25 | 323ADISP_SEQ: 0001F024 | E0329 | Support ST/Hynix 2K flash Samsung K9F4G,K9K8G,K9L8G |
| 2005/03/30 | 323AD_SEQ: 2005-03-29 | 323ADISP_SEQ: 0001F5D1 | E0330 | Fix ST/Hynix twin mode |
| 2005/04/01 | 323AD_SEQ: 2005-03-31 | 323ADISP_SEQ: 0003B5E9 | E0401 | ST/Hynix set to 20MHz Fix ST/Hynix twin mode |
| 2005/04/04 | | | E0404 | ST/Hynix 2K set to Read 30 Write 20 |
| 2005/04/07 | | | E0407 | Samaung K9K4G change to R/W 40MHz |
| 2005/04/07 | | | E0407 V2 | Add support K9L8G twin mode Add Led Ready Led busy setting |
| 2005/04/09 | | | E0409 | Add Flash Database file |
| 2005/04/15 | 321AC: | 321AC:00032883 | E0409 | 321AC WHQL HCT12.0 pass |



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| | 2005-04-13 | | | |
| 2005/04/20 | 321AB: 2005-04-19 321AC: 2005-04-20 | 321AB:0002014F 321AC:0003263E | E0420 | 1. Test Program add recognize USB Hub port function 2. 321AB fix win2k auto run 3. 321AC fix run memorybar partition plug-in show security partition |
| 2005/04/21 | | | E0421 | 1. twin mode dbf file setting (@ : single mode ! : twin mode) |
| 2005/04/22 | | | E0422 | 1. Win98 VIA Config hub problem |
| 2005/04/27 | | | E0427 | 1. 321AC after test OK LED flash |
| 2005/04/29 | | | E0429 | 1. add 321acptest.bin (Pretest) 2. fix 321AC after test LED no flash 3. test different flash on the same time |
| 2005/05/03 | 323ADISP: 2005-05-02 323ADISP_SEQ: 2005-05-02 | 323AD: 3B4FD 323AD_SEQ: 3B9A1 | | 1.DevEsti Program can't detect 323AD |
| 2005/05/04 | 321ACISP: 2005-04-25 | 321AC: 35E96 | E0504 | TestAP: auto scan usb when plug-in 321AC ISP :fix suspend current |
| 2005/05/09 | | | E0509 | TestAP: Fix 0504 run 321AB and 320AC show error message |
| 2005/05/12 | 321ACISP: 2005-05-02 | 321AC:3B4FD | E0512 | TestAP: Add Scan show bad block number 321ACISP: fix 321AC+Micro junior fail |
| 2005/05/17 | | | E0517 | TestAP:Display show badblock error |
| 2005/05/20 | | | E0520 | TestAP: 1. fix multi-device make auto run , only one device have image |



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| | | | | <ul style="list-style-type: none"> 2. Remove Win2k unsafe remove message 3. E0517 pretest bad block total over 20 then fail problem |
| 2005/05/24 | 323AD: 2005-03-15 323AD_SEQ: 2005-03-31 | 323ADISP :0003AD79 323ADISP_SEQ: 0003B5E9 | | E0503 323ADISP Mac 9.2 can't recognize |
| 2005/05/26 | | | E0526 | TestAP: 1. add format retry 2. add erase info block before pretest |
| 2005/06/01 | | | E0601 | TestAP: 1. add write protect check before pretest 2. modify 321AC test program check bad block |
| 2005/06/08 | | | E0608 | TestAP: 1. add a reset after download ISP , for k9w8g usb 1.1 unformat problem 2. add win98 no config hub can test 3. remove 0x21 vendor command in ReadConfig Button . |
| 2005/06/16 | | | E0608 | 1. E0608 add reset make 321AC after test LED no Flash , update ledproc321ac.bin |
| 2005/06/23 | | | E0623 | <ul style="list-style-type: none"> 1. Check bad block can support MU large then 16 2. Hynix 8G twin 323AD , Run E0608 ISP no download |
| 2005/06/24 | 321AC 2005-06-24 | 321AC: 00051C99 | E0624 | <ul style="list-style-type: none"> 1. add 321AC partial flash support 2. 321AC support micron interleave |
| 2005/07/01 | 321AC 2005-07-01 | 321AC: 00050CCA | E0701 | <ul style="list-style-type: none"> 1. add a partial flash and normal flash check box , if no check then capacity is calculate by bad block , if check then capacity is calaulate by partial size 2. 321ac ISP fix partial flash autorun function |
| 2005/07/07 | | | E0707 | <ul style="list-style-type: none"> 1. add preload include sub directory function 2. fix Win2000 test multi-device OK , plug-out usb show "Get IC Version Fail" |



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| 2005/07/15 | 321AC_NR 2005-07-15 | | E0715 | 1. add SM321ACISP_NR.BIN isp for hynix NR chip |
| 2005/07/25 | | | E0725 | 1. add time out setting |
| 2005/07/27 | | | E0727 V2 | 1. add 321acptest_nr.bin for Hynix NR Pretest 2. fix 323AD ST Hynix some time copy compare fail |
| 2005/08/02 | | | E0802 | 1. add 321AC support Hynix NR ISP |
| 2005/08/04 | | | E0804 | 1. fix Preload file too large problem 2. add micron partial flash 3. fix get flash dbf file only get CEO |
| 2005/08/08 | | | E0808 | 1. modify partial size setting from M bytes to Block 2. fix 0804 dbf file bug |
| 2005/08/11 | | | E0811 | 1. 321AC If have ISP then pretest fail 2. add hynix 512 downgrade |
| 2005/08/12 | | | E0812 | 1. Update Config Hub function just need one scan then config |
| 2005/08/15 | 321AC 2005-07-01 | 321AC: 0005633B | E0815 | 1. customer = 1 old memorybar , 8 new memorybar 2. update 321acisp for old and new memorybar |
| 2005/08/17 | 323AD: 2005-08-17 323AD_SEQ: 2005-08-17 | 323ADISP :0004B7CD 323ADISP_SEQ: 0004CB57 | E0817 | 1. 321AC K9WAG twin mode pretest fail 2. 323AD IBM NoteBook suspend resume |
| 2005/08/18 | 323AD: | 323ADISP: | E0817 | 1. 323AD IBM NoteBook i1200 cold boot Problem |



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| | 2005-08-18 323AD_SEQ: 2005-08-18 | 00050D41 323ADISP_SEQ: 000520CB | | |
| 2005/08/19 | 323AD: 2005-08-19 323AD_SEQ: 2005-08-19 | 323ADISP: 00050EDB 323ADISP_SEQ: 00052C21 | E0817 | 1. 323AD fix chkdsk problem |
| 2005/08/24 | | | E0824 | 1. add AGAND 4G support |
| 2005/09/05 | | | E0905 | 1. fix 0812-0824 config hub can't scan large 2 hub . 2. add readback test program version . 3. add when > 2G format to FAT32 , (but preload function only support fat 16) |
| 2005/09/09 | | | E0909 | 1. add Scan capacity function 2. change partial size setting form block to M byte |
| 2005/09/13 | | | E0913 | 3. add back removeable type have partition table , because linux can't mount |
| 2005/09/15 | | | E0915 | 1. modify test program UI show bmp location |
| 2005/09/16 | | | E0916 | 1. add usb 1.1 button 2. modify scan capacity |
| 2005/09/21 | | | E0921 | 1. modify pretest string "No/RW Test" to "Erase only" |
| 2005/09/22 | 323ad_SEQ: 2005-09-22 | 323adisp_seq: 00053729 | E0921 | 1. update 323adisp_seq.bin for check sum error problem 2. update infupdate for no copy file to drivers folder |
| 2005/09/28 | | | E0928 | 1. after test ok , unplug 1 usb , don't refresh test result |
| 2005/10/05 | | | E1005 | 1. modify show icon 2. modify 321acptest.bin for intlv mode second flash no reference default bad block |
| 2005/10/21 | | | E1021 | 1. update smi32x2k.sys for avoid blue screen |



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| | | | | 2. fix preload file cluster different bug |
| 2005/10/25 | | | E1025 | 3. New UI |
| 2005/10/26 | | | E1026 | 1. UI display on 800X600 |
| 2005/10/28 | | | E1028 | 1. UI modify 2. open down grade setting bad block 44 limit |
| 2005/11/02 | | | E1102 | 1. fix E1028 AGAND can't set BB less then 44 |
| 2005/11/10 | | | E1109 | 1. add 332aa support 2. add copy test % select 3. add font size can adjust |
| 2005/11/11 | | | E1111 | 1. add downgrade flash auto capacity |
| 2005/11/15 | | | E1115 | 1. add auto install driver 2. list can zoom to all window |
| 2005/11/18 | | | E1118 | 1. fix E1111 downgrade auto capacity |
| 2005/11/30 | | | E1130 | 1. ignoring MU check when downgrade flash 2. Duplicating write command after DoReset for K9G4G flash to solve LBA0 without FORMAT. 3. CLlCtrl Column Show and Hide. 4. Update Udisk information after test finish. 5. Add double click in ClistCtrl Item for reading CID information. 6. Decreasing refresh times when plug or un-plug udisk. 7. Edited copy compare percentage show when password failed. 8. Remember last column width in ClistCtrl. |



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| | | | | <ul style="list-style-type: none"> 9. Disable "Open Device Fail" Message. 10. Disable ClistCtrl column Auto-resize. |
| 2005/12/1 | | | E1201 | <ul style="list-style-type: none"> 1. Update SM321ACPTTEST_micron.bin 2. Support K9HAG formatting with FAT32.(Label will exist) 3. Support Autorun+Public+Private 3IN1 option with APP3IN1=1 in INI file. |
| 2005/12/2 | | | E1202 | <ul style="list-style-type: none"> 1. Update the INF Update program to solve losing detect UFD with factory driver in win2000.(Example: plug 14 UFDs but showed 13 UFD in Device Manager) |
| 2005/12/5 | | | E1205 | <ul style="list-style-type: none"> 1. Fixed the port # (under item column) become "090c/1000" 2. Fixed not refresh "final actual" capacity, until 1 UFD is unplugged (or rescan) 3. Fixed "Open Device Fail" problem. 4. Add user setting the EndHead, EndSector and End Cylinder information. |
| 2005/12/8 | 321ACISP: 2005-12-08 | 321ACISP: 00057D74 | E1208 | <ul style="list-style-type: none"> 1. Update the SM321ACISP.BIN for AUTORUN Duplication fixing in WinXP. |
| 2005/12/12 | | | E1212 | <ul style="list-style-type: none"> 1. Add the setting of the maximum length of serial number in setting dialog. 2. Update the erasing processing before pretest for K9L8G flash. |
| 2005/12/16 | 321ACISP: 2005-12-16 | 321ACISP: 00057FE0 | E1216 | <ul style="list-style-type: none"> 1. Update the SM321ACISP.BIN for Autorun three times in win2k sp4 with usb1.1 2. Erasing ISP process before pretest for SM322AD. |
| 2005/12/22 | | | E1222 | <ul style="list-style-type: none"> 1. Add Serial Number for SM340 2. Support SM324BA |
| 2005/12/23 | 324BAISP: 2005-12-23 | 324BAISP: 0012F131 | E1223 | <ul style="list-style-type: none"> 1. Update the SM324BAISP.BIN 2. Update the Checking of CardMode for 512 and 2K |
| 2005/12/28 | 324BAISP: 2005-12-26 | 324BAISP: 00137A03 | E1228 | <ul style="list-style-type: none"> 1. Update SM324BAISP.BIN for Read Write Clock separate setting . |
| 2006/01/10 | | | F0110 | <ul style="list-style-type: none"> 1. Modify the pretest string. |



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| 2005/12/29 | 321ACIAP: 2005-12-29 | 321ACISP" 0006BCA9 | E1228 | 1. update 321acisp.bin for autorun + memorybar bug. |
| 2006/1/12 | 324BAISP: 2006-01-12 | 324BAISP: 0014D8F2 | F0112 | 1. Modify the ScanUSB button only if 「102」 caption became non zero in string table. 2. Modify the Serial Number process with mask and auto update with date. |
| 2006/01/13 | | | F0113 | 1. If autorun image too large , program will crash . |
| 2006/01/17 | 324BAISP: 2005-01-16 | 324BAISP: 00014D932 | F0117 | 1. update 324ISP for copy compare fail 2. update 324 pretest isp 3. add 2 flash to 321 dbf file |
| 2006/01/19 | | | F0119 | 1. update AGAND downgrade pretest , for some time capacity reduce half 2. modify check bad block only when do pretest |
| 2006/01/20 | | | F0120 | 1. modify sm32xtest2.exe add start button |
| 2006/01/26 | | | F0126 | 1. remove 323AD reset 2. add infineon 0x66 force ID 3. preload data problem |
| 2006/01/27 | 321ACK9HAGISP: 2006-01-27 | 321ACK9HAGISP: 00068861 | F0127 | 1. fix K9HAG miss compare problem |
| 2006/02/15 | 321ACPTTEST: 2006-02-15 | | F0215 | 1. 321AC add hynix MLC pretest check last 2 page default bad block 2. 324BB add dbf K9F4G K9K8G K9WAG pll setting 3. 324BB small block flash download isp problem 4. if no select cid then can't select auto run |
| 2006/02/16 | 324BBISP: | | F0215 | 1. 324BB MLC twin mode support |



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| | 2006-02-15 324BBPTEST: 2006-02-16 | | | 2. mp tool show error card mode (mlc , slc), |
| 2006/02/20 | 324BBPTEST: 2006-02-20 | | F0215 | 1. modify pretest blkn1 ,blkn2 |
| 2006/02/22 | 324BBISP: 2006-02-22 | | F0215 | 1 . K9K8G add erase cid before pretest 2 . K9K2G copy compare fail. |
| 2006/02/23 | 324BBISP: 2006-02-23 | | F0215 | 1. modify 324 dbf k9k8g , k9f4g clock to 0x13 2. modify 324bb isp for mlc twin mode |
| 2006/02/24 | 324BBISP: 2006-02-24 | | F0215 | 1. modify 324BB isp K9L8G 2 plane mode , format ok , power off , then unformat |
| 2006/03/02 | | | F0302 | 1. 321BB mp tool load wrong dbf file |
| 2006/03/06 | 324BBISP: 2006-03-05 | | F0302 | 1. 324BB + K9HAG 2 plane mode , no format problem |
| 2006/03/07 | 321ACK9HAGISP: 2006-03-07 321BBISP: 2006-03-06 324BBISP: 2006-03-07 | | F0307 | V. 1.1. 321AC K9HAG partial write limit to 1 V. 1.1. 321BB small block usb enum problem V. 1.1. 324BB K9NBG support 4. fat32 change reserve 0x26 to 0x20 |
| 2006/03/08 | | | F0308 | 1. fat16 and fat32 format modify 2. 321BB+infineon HYF33DS 3. 321BB+Toshiba TC58NVG1D4B |



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| 2006/03/10 | | | F0310 | <ol style="list-style-type: none"> 1. fat16 and fat32 for mac os 10.1 2. 321BB card mode 3. 321AC downgrade flash check bad block |
| 2006/03/14 | 321ACISP: 2006-03-14 | 321ACISP: 00062048 | F0314 | <ol style="list-style-type: none"> 1. 321AC hynix MLC , Samsung MLC . miss compare problem 2. add 324 ST MLC dbf file |
| 2006/03/15 | 321ACISP: 2006-03-15 | 321ACISP: 000624ED | F0315 | <ol style="list-style-type: none"> 1. MP tool format totalsector -1 2. fix 0314 321acisp 2K MLC fat have data problem |
| 2006/03/16 | 321BBISP: 2006-03-16 324BBISP: 2006-03-16 | 321BBISP: 0006CE97 324BBISP: 0017F795 | F0316 | <ol style="list-style-type: none"> 1. MP tool add every mu copy test 2. fix 324BB usb 1.1 mp tool pretest fail 3. fix 324BB and 321BB on 2K MLC fat data problem 4. add 324 0xc1 0x76 force id |
| 2006/03/17 | 324BBISP: 2006-03-17 | 324BBISP: 00018266C | F0317 | <ol style="list-style-type: none"> 1. MP too preload long file name 2. 324BB Mac format , mac extension hang |
| 2006/03/24 | 324BBISP: 2006-03-24 | 324BBISP: 00017B9A3 | F0324 | <ol style="list-style-type: none"> 1. 324BB ISP fix when read data insert inquiry command then data error problem |
| 2006/03/25 | | | F0325 | 1 add SM324BBPTEST_SMIDriver.BIN file for SMI driver pretest fail problem |
| 2006/03/28 | | | | 1.fix 321BB AGAND pretest reference default bad block problem |
| 2006/03/31 | 321BBISP: 2006-0331 324BBISP: 2006-0331 | 321BBISP: 0054144 324BBISP: 017CE47 | F0331 | <ol style="list-style-type: none"> 1. 321BBISP , add multi-lun string 2. 324BBISP , add multi-lun string 3. 324BBISP-R , for AGAND flash 4. MP tool , 321BB test pass led flash |
| 2006/4/3 | | | F0403 | <ol style="list-style-type: none"> 1. Add USBDisk Image for preload function. |



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| | | | | 2. Fix AGAND Flash download 16K isp if isp size more than 16k for SM324BB |
| 2006/04/06 | 321BBISP: 2006-0406 | 321BBISP: 0053FB6 | | 1. 321BBISP for warm boot device not recognize problem |
| 2006/04/07 | 321BBISP: 2006-0407 321AC2KMLCISP: 2006-0407 | 321BBISP: 0005AD44 321AC2KMLCISP: 00063708 | F0406 | 1. Fix SM321BB reboot issue. 2. Fix SM321AC miss compare with K9HAG. 3. Update Flash_321BB.DBF and Falsh_324.DBF 4. Fix Format function in the MPTool. Let First data sector position in the multiple of 4. |
| 2006/4/10 | 321ACISP: 2006-04-10 321BBISP: 2006-04-10 | 321ACISP: 0006F7FC 321BBISP: 0005ACF8 | F0406 | 1. Fix the pretest for SM324BB-R. 2. Add the multi lun string for SM321AC. 3. Fix SM321BB reboot issue (0407 still fail) |
| 2006/4/12 | | | F0412 | 1. Change the MLC bad block information from last page. 2. Fixed the SM321BB copy-compare issue when MP tool running |
| 2006/4/20 | 321ACISP 2006-4-20 321BBISP 2006-4-20 | 321ACISP 00060806 321BBISP 00069CF7 | F0420 | 1. Modify the SM32x DBF file for the timing setting 2. Insert 0x85 command for the Micron 70nm Flash |
| 2006/4/21 | 323ADISP 2006-4-21 | 323ADISP 00052445 | | 1. Add the SM323 auto run + memory bar |
| 2006/4/25 | 324BBISP 2006-04-25 | 324BBISP 0019867A | F0425 | 1. Insert 0x85 command for the Micron 70nm Flash 2. Preload in FAT32 format 3. Improve SM324+K9F4G for the performance in 2-plane mo |



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| 2006/5/4 | 324BBISP 2006-05-04 321AC2KMLC 2006-04-26 | 324BBISP 00198064 321AC2KMLC 000638A1 | F0504 | <ol style="list-style-type: none"> 1. SM321AC ISP fixed auto-run for the 2K MLC in Win2000 2. SM324BB ISP fixed the copy-compare failure in WinMe with 2-plane mode (2pcs of the K9F4G) 3. Fixed the automatics turn-off MU option for the Spectek flash 4. Fixed Win2000 copy-compare error when the MP tool run in the background 5. Modify the DBF file to correct the Hynix MLC/SLC Flash setting |
| 2006/05/15 | SM321ACISP 2006-05-11 SM321BBISP 2006-05-15 | SM321ACISP 00061B16 SM321BBISP 0006B14F | F0515 | <ol style="list-style-type: none"> 1. Adjust the format size for the 128MB and 256MB as the 0315 2. Disable MP tool copy-compare when enable the auto-run function 3. SM321BB randomly copy-compare failure during full size copy and plug-in/plug-out several times 4. Disable the copy-back function for the Hynix MLC |
| 2006/05/22 | | | F0522 | <ol style="list-style-type: none"> 1. Fixed the capacity inconsistency for the SM321E(SM321BB) |
| 2006/06/15 | SM321BBISP 2006-06-15 SM324BBISP 2006-06-15 SM323ADISP 2006-06-01 | SM321BBISP 00086AA8 SM324BBISP 00198C8A SM323ADISP 00052A96 | F0615 | <ol style="list-style-type: none"> 1. SM321BB support the Hynix HY27UV08AG5M interleave mode 2. Update the DBF file for correcting the flash option 3. SM324BB fixed the Samsung K9K1G08U0M power-off capacity didn't match 4. SM321BB and SM324BB support the Samsung K9G8G 5. SM323AD correct the product string didn't show up 6. MP tool correct the FAT32 format as the WinXP cluster size |



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| 2006/06/19 | SM321BBISP 2006-06-17 | SM321BBISP 00087027 | F0619 | <ol style="list-style-type: none"> 1. SM321BB fixed HY27UV088G5M interleave mode in WHQL Syscache 2. SM321BB fixed MLC interleave pretest bad block number |
| 2006/07/18 | SM324BBISP 2006-07-18 | SM324BBISP 00199C1C | F0718 | <ol style="list-style-type: none"> 1. SM324ISP for the Samsung K9F2G08U0A 2. Improve the SM324 with MLC flash performance 3. The MP tool fixed the some SM324 controller can't complete all testing steps in the second time. 4. Update the dbf files for the new flash |
| 2006/08/21 | SM321ACISP 2006-08-14 SM321BBISP 2006-08-18 SM324BBISP 2006-08-17 | SM321ACISP 0006AD34 SM321BBISP 0008EAF5 SM324BBISP 001D3828 | F0817 | <ol style="list-style-type: none"> 1. SM321AC support the Samsung K9K8G interleave mode 2. SM321AC and SM321BB fix the CD-Creator compatibility 3. SM321AC support the Samsung K9F2G08U0A and K9LAG 4. SM321BB support the MSC USBCV r1.3 for the case13 5. SM321BB improve the MLC performance in Win2K 6. SM324BB support the multi-luns function 7. The MP tool support the CDROM size to 32GB 8. The MP tool fix the preload problem in FAT32 format |
| 2006/08/30 | SM324BBISP 2006-08-29 | SM324BBISP 001D2F5D | F0830 | <ol style="list-style-type: none"> 1. SM324BB add the floppy drive supporting 2. SM324BB MP tool for the multi-device testing unstable by using factory driver 3. SM321BB for the ignore "status checking" by the SM321bbisp_2k_nr.ISP 4. The MP tool add the optional for the FAT32 format 5. The MP tool add the preload to Floppy drive |



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| 2006/09/13 | SM324BBISP 2006-09-13 SM321BBISP 2006-08-17 | SM324BBISP 001D6EBF SM321BBISP 00086D00 | F0913 | <ol style="list-style-type: none"> 1. SM324 fix the copy-compare bug for the K9F2G08U0A 2. SM324 MP tool's factory avoid the testing stuck when one device failed 3. Support the Powerchip P1U1GR30AT 4. DBF setting for the Intel 72nm Flash: 29F04G08AANB1, 29F08G08BANB1, and 29F08G08FANB3 5. The SM321BB remove the case 13 parts |
| 2006/09/29 | SM324BBISP 2006-09-15 SM321BBISP 2006-09-28 | SM324BBISP 001DBA67 SM321BBISP 00086DB3 | F0928 | <ol style="list-style-type: none"> 1. Support the AGAND Flash 1GB can be used just half size 2. SM324 -R ISP for the AGAND capacity increase to 485MB 3. Support the Specteck auto disable MU 4. SM321BB ISP fix the interleave mode for the MLC Flash 5. MP tool add the option for the K9F2GU0A single plan or two plane mode in the default.ini to cover too many bad block# in two plane |
| 2006/10/05 | SM321BBISP 2006-10-05 SM324BCISP 2006-10-04 | SM321BBISP 00086016 SM324BC 0004CC8F | F1005 | <ol style="list-style-type: none"> 1. SM321BB fix the MAC 10.4.2 can't recognized 2. SM324BC release |
| 2006/10/20 | SM321BBISP 2006-10-20 | SM321BB_2KMLC 000B7401 | F1020 | <ol style="list-style-type: none"> 1. SM321BB_2KMLC support the Samsung 63nm MLC (K9G8G, K9LAG, K9HBG, K9MCG) NOP=1 2. MP tool fix the AGAND pretest second time failed 3. MP tool factory driver for the SM324BC |
| 2006/10/25 | SM324BCISP 2006-10-20 | SM324BCISP 0006A6AB | F1025 | <ol style="list-style-type: none"> 1. SM324BC pretest will not erase default bad block info 2. MP tool add flash ID 4bytes info |



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| | | | | <ul style="list-style-type: none"> 3. MP tool reduce the AG-AND pretest time and correct bad block 4. MP tool extend the CheckSize option from 4GB to 16GB |
| 2006/10/27 | SM321ACISP 2006-10-25 SM321ACISP 2006-10-27 SM324BBISP 2006-10-26 SM324BCISP 2006-10-26 | SM321AC_2KMLC 000BD883 SM321ACISP 0006ADB4 SM324BBISP 001E12AC SM324BCISP 0006AA97 | F1027 | <ul style="list-style-type: none"> 1 SM321AC support NOP=1 for the Samsung MLC 2 SM321AC fix write-protect in the MAC 10.3.x 3 SM324BC fix the copy-compare error for the K9G8G and K9MCG 4 SM324BB fix the SM324 LED blanking abnormal status 5 SM324BC correct the right capacity 6 MP tool fix the SM321BB for the Micron MLC bad block number 7 SM324BB – R for the AG-AND CID/ISP corruption |
| 2006/11/03 | SM321ACISP 2006-11-02 SM324BCISP 2006-11-03 | SM321AC_2KMLC 000BD712 SM324BCISP 0006E20F | F1103 | <ul style="list-style-type: none"> 1. SM321AC NOP=1 ISP for the memory bar security mode 2. MP tool fix the SM321AC for the flash bad block info with different card mode 3. Increase Samsung MLC access cycle time from 50ns to 30ns 4. SM324BC ISP to increase the small file copying speed. 5. SM324BC ISP modify the Max. bad block number |
| 2006/11/14 | SM321BBISP 2006-11-13 SM324BCISP 2006-11-13 | SM321BB_2KMLC 000B870C SM324BCISP 000749A4 | F1114 | <ul style="list-style-type: none"> 1. MP tool re-organized the subdirectory for the each product 2. MP tool check the Hynix MLC in the last page or last page –2 3. Set the Hynix MLC clock to 0x06 (82ns) 4. MP tool to display correct bad block number for the SM324BC 5. SM321BB NOP=1 for mark the bad block for the mode changes of the Samsung MLC 6. SM324BC max. the bad block number |



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| | | | | <ol style="list-style-type: none"> SM324BC for the Renesas single channel supporting SM324BC fixed the H/W write protect problem in WinMe and Win98 SM324BC will not check the RB0/RB1 for the 2-plane mode. |
| 2006/11/29 | SM321ACISP 2006-11-29 SM321BBISP 2006-11-29 SM321BBISP 2006-11-29 SM324BCISP 2006-11-27 | SM321AC_2KMLC 000BDD01 SM321BBISP_2KMLC_MSC 000B1D42 SM321BBISP_2KMLC 000B9080 SM324BCISP 0007E79D | F1129 | <ol style="list-style-type: none"> SM321AC NOP=1 ISP fixed the memory bar application problem in Samsung K9G8G SM32x NOP=1 ISP fixed the power on re-build table SM324BC fixed the reading of the default bad block MP tool changed of the SM324BB and SM324BC clock setting MP tool add the user specific Flash part number option MP tool correct the SM324BC's reserved max. bad block to meet the Flash spec. (SLC 2%, MLC 4%, Samsung MLC 2.5%) MP tool choose the auto run in SM321BB will disable the MSC |
| 2006/12/19 | SM321BBISP 2006-12-15 SM321BBISP 2006-12-15 SM324BBISP 2006-12-15 | SM321BBISP_MSC 0008AF2F SM321BBISP 00084BF0 SM324BBISP 001E1AAF | F1219 | <ol style="list-style-type: none"> SM321BB ISP to support the Samsung K9F1G08U0B SM321BB ISP to support the Vista RTM Auto-run SM324BB ISP for the Micron/Intel MT29F2G08AAC, MT29F4G08AAA, MT29F4G08BAB for the performance F1219 MP tool to avoid the SM321BB MSC and non-MSC ISP loading F1219 MP tool had full save the parameters in INI file with "save as" to user ini file. F1219 MP tool to support the preload file with the FAT12 F1219 MP tool to support the user setting for the max. bad block numbers in default.ini by the "original BadBlock=1" |



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| | | | | 8. F1219 MP tool to support the Samsung K9F1G08U0B 9. F1219 MP tool's Auto-capacity option to have all good blocks 10. DBF File changes for the SM321AC, SM321BB, SM324BB, and SM324BC <ul style="list-style-type: none"> a. Samsung K9LAG08U0M, K9HBG08U1M, K9F1G08U0B b. Hynix HY27UV08AG5M, HY27US08121B c. ST NAND08GW3B2AN6, NAND04GW3B2BN6 NAND02GW3B2AN6 d. Micron MT29F8G08BAA, MT29F16G08FAA, MT29F8G08FAB, MT29F8G08FAC e. Intel JS29F04G08AANB1, JS29F16G08FANB1, JS29F08G08BANB1, JS29F08G08FANB3, JSF32G08FAMB1, JSF16G08CAMB1 JSF08G08AAMB1 |
| 2007/01/23 | SM321BBISP 2007-01-22 SM321BBISP 2007-01-22 SM324BCISP 2007-01-23 SM324BBISP 2007-01-26 | SM321BBISP_2KMLC 000B6CDF SM321BBISP_2KMLC_MSC 000 B795B SM324BCISP 0008F45A SM324BBISP 001E1C8D | G0117 | 1. SM321BB and SM324BC ISP for the Vista ready Boost read performance 2. SM324BB ISP fix the Micron SLC flash write timing 3. SM324BC ISP fixed the Case13 for the IMI tester 4. SM324BC ISP fixed the WHQL "frame work" 5. MP tool add option "justscan" in the default.ini for the supervisor mode 6. MP tool support the random serial number and 13 bytes 7. MP tool display the % of the total reserved block 8. MP tool fixed the copy-compare when enabling Floppy mode |



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| 2007/02/09 | SM321BBISP 2007-02-02 SM324BCISP 2007-02-09 SM324BBISP 2007-02-09 | SM321BBISP_MSC 00084AE6 SM324BCISP 00098787 SM324BBISP 001E20F0 | G0209 | <ol style="list-style-type: none"> 1. Enhance the SM321BB for the SLC ready-boost read performance. 2. SM321, SM321BB, and SM324BC support the Hynix 60nm MLC NOP=1 3. SM324BC ECC over 4-bit will mark bad block 4. MP tool fixed the preload for the long directory name (>11bytes) 5. SM324BB fixed LED blink when SCSI 0x25 6. DBF add the new flash setting: Hynix HY27UT088G2M, HY27UU08AG5M, Micron MT29F16G08QAA, Samsung K9F1208U0C, K9L8G08U0A, Intel JSF16G08CAMB2, JSF08G08AAMB2, JSF32G08FAMB2 |
| 2007/02/12 | | | 2007/02/12 | <ol style="list-style-type: none"> 1. MP tool and DBF for Hynix HY27UV08BG5M 2. Slow the SM321AC clock to 0x04 for the Samsung K9WAG08U1A 3. Disable the SM321AC copy-back for the Micron 29F08G08CANB1 |
| 2007/03/13 | SM321BBISP 2007-03-13 SM321BBISP 2007-03-13 SM324BCISP 2007-03-07 SM324BCISP 2007-03-07 | SM321BBISP_MSC 000923FD SM321BBISP_2KMLC_MSC 000BB139 SM324BCISP 00099D9B SM324BCISP_HBG 0009C6D8 | V1.16.04 | <ol style="list-style-type: none"> 1. SM321BB support the Samsung K9F1208U0C 2. SM324BC set max. bad block to 56 will caused Vista format failed 3. SM324BC with K9F2GU0A had bad block in 0x7A~0x7E location caused "can't recognized" 4. SM321BB and SM324BC improve Vista ready boost first time read performance 5. SM324BC special ISP for the Samsung HBG to have <100mA operation current |



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| | SM321BCISP 2007-03-09 | SM321BCISP 000B4BBE | | 6. Add SM321BC ISP 7. MP tool change to date to version number V1.16.04 8. MP tool add the "DISKREADONLY" option in the default.ini |
| 2007/04/11 | SM324BCISP 2007-04-10 SM324BCISP 2007-04-04 | SM324BCISP 0009FE02 SM324BCISP_HBG 0009E383 | V1.16.11 | 1. MP tool support "Erase bad+55/AA option". 2. MP tool fixed MAC 10.1.5 with FAT32 format in G0313 3. MP tool fixed the Toshiba MLC bad block restoring. 4. MP tool fixed the each Single plane bad block display caused the MP tool display crash issue. (with K9NBG) 5. SM324BC ISP add the mark bad block when ECC error and not to disconnect the USB 6. SM324BC ISP fixed the power on save link to avoid the big file miss compare 7. SM324BC ISP fixed Micron MT29F8G08MAA mis-compare by repeat bad block |
| 2007/05/07 | SM321BBISP 200-04-23 SM321BBISP 2007-05-07 SM321BBISP 2007-05-07 SM324BCISP 2007-04-20 SM324BCISP | SM321BBISP 00090700 SM321BBISP_MLC_2plane 0017C03C SM321BBISP_4K 0018CD58 SM324BCISP 000AB47F SM324BCISP_4K | V1.16.16 | 1. SM321BB ISP fixed autorun showed "restart" in first time with some PC platforms. 2. SM321BB ISP add 2-plane mode for the MLC Flash to double the 2-plane MLC performance 3. SM324BC ISP fixed the bad block repeat marking. 4. SM324BC ISP fixed the plug-out/plug-in lost 64KB copy-back in the SLC copy-back mode. 5. SM324BC and SM321BB ISP add the Samsung 4KB/page flash (K9F8G08U0M & K9GAG08U0M) supporting (Beta version) for |



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| | 2007-04-11 | 00181E3A | | <p>single channel.</p> <p>6. MP tool add the 2-plane mode flash force ID</p> <p>7. MP tool check max. reserved bad block number to have enough spare block.</p> <p>8. MP tool will erase ISP when ISP download error</p> |
| 2007/06/08 | SM321BBISP 2007-06-08 SM324BCISP 2007-06-04 SM324BCISP 2007-06-07 SM324BCISP 2007-06-07 | SM321BBISP_4K 0018A992 SM324BCISP 000D0C77 SM324BCISP_4K_twin 0017CDE4 SM324BCISP_4K_sinlge 00175094 | V1.16.19 | <p>1. MP tool add the Samsung 4KB/page dual channel supporting (K9F8G and K9GAG)</p> <p>2. MP tool's default.ini add 2 options for the wear-leveling count parameters n (count number = nx256). Defaults: ISP count 10K (n=40), and Data Write count 2K (n=8).</p> <p>3. SM324BC ISP to support the new wear-leveling</p> <p>4. DBF file disable Intel MLC cache mode: JS29F16G08CAMB1 JS29F08G08AAMB1 JS29F16G08CAMB2 JS29F08G08AAMB2 JS29F32G08FAMB1 JS29F32G08FAMB2</p> <p>5. DBF file disable the "read programming status" checking for all MLC in SM324BC</p> <p>6. DBF file add new flash setting: K9G2G08U0M, K9L4G08U1M, MT29F4G08AAC, NAND08GW3C2A, NAND16GW3C2A</p> |
| 2007/06/29 | SM324BC 2007-06-29 SM324BC 2007-06-29 | SM324BCISP_4K_twin 001A2E25 SM324BCISP_4K_sinlge 00199753 | V1.16.22 | <p>1. SM321BB and SM324BC support the Hynix 60nm date code after 726A. the date code between 712A to 726A need to use the other special ISP.(test_321_324_Hynix_60nm_G0625).</p> <p>2. SM324BC fixed the mark the same bad block when programming</p> |



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| | SM324BC 2007-06-25 SM321BB 2007-06-26 | SM324BCISP 000 D3E6F SM321BBISP_2Plane 0018D19E | | <p>failed</p> <p>3. SM324BC fixed the 4KB/page copy-comparison failed in USB1.1</p> <p>4. SM324BC fixed the 2x K9F8G burn-in failed</p> <p>5. SM324BC add the new wear-leveling</p> <p>6. SM321BB and SM324BC fixed the GPS can't recognized by command 1A to check length is at dCBWTransferLength, not length in CBWCB</p> <p>7. MP tool fixed the bad block information for the force ID mode flash when pretest</p> <p>8. MP tool Support the Samsung K9KAG08U0M, K9LBG08U0M, K9WBG08U1M.</p> <p>9. DBF add new flash setting: NAND08GW3C2AN1 NAND08GW3C2AN6 NAND16GW3C2AN1 NAND16GW3C2AN6</p> |
| 2007/07/10 | SM324BC 2007-07-09 SM324BC 2007-07-09 SM321BB 2007-07-10 | SM324BCISP_4K_SINGLE 00199733 SM324BCISP_4K_TWIN 001A2DE7 SM321BBISP_2Plane 0018D2DE | v.1.16.24 | <p>1. SM324BC fixed the G0629 USB-IF MSC problem for the Samsung 4KB/page Flash</p> <p>2. SM321BB fixed the G0629 bug for the auto-run function in 2-plane mode</p> <p>3. MP tool for data area start LBA for the 32x for the Samsung 4KB/page performance</p> <p>4. MP tool supports the ST NAND08GW3C2AN</p> |
| 2007/08/02 | SM324BC 2007-08-02 SM324BC | SM324BCISP_4K_SINGLE 001C0406 SM324BCISP_4K_TWIN | v.1.16.26 | <p>1. SM324BC improved the Samsung 4KB ISP wear-leveling</p> <p>2. SM324BC fixed the Samsung 4KB power-on-off problem</p> <p>3. SM324BC support the external Interleave mode for the Samsung</p> |



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| | 2007-08-02 | 001CD4D0 | | <p>4KB 2CE Flash (K9WBG, K9HCG) to have better performance</p> <p>4. MP tool changed the SM324BC Samsung 4KB capacity to consistence</p> <p>5. DBF file add the ST NAND256W3A0AN6 and NAND512W3A2BN6</p> |
| 2007/08/28 | SM321BB | SM321BBISP | v.1.16.29 | 1. SM321BB ISP fixed WinCE 5.0 un-recognized |
| | 2007-08-22 | 0009FF77 | | 2. SM321BB ISP fixed the mode sense ReadTOC function for the Vista auto-run |
| | SM321BB | SM321BBISP_MSC | | |
| | 2007-08-10 | 000975BD | | 3. SM321BB ISP support the Samsung K9G2G08U0M |
| | SM321BB | SM321BBISP_2KMLC | | 4. SM324BC ISP fixed CMD_21_F0 read the link-table problem |
| | 2007-08-22 | 000BE5BF | | 5. SM324BC ISP improve the performance for the Samsung 4KB flash |
| | SM321BB | SM321BBISP_2KMLC_MSC | | |
| | 2007-08-10 | 000C0377 | | 6. MP tool support the Samsung K9G2G08U0M |
| | SM321BB | SM321BBISP_4K | | 7. MP tool support the single-plane bad block number |
| | 2007-08-10 | 0018B3F5 | | 8. MP Tools check UFD size up to 64GB |
| | SM321BB | SM321BBISP_2PLANE | | |
| | 2007-08-22 | 0019E32E | | |
| | SM321BB | SM321BBISP_2KMLC_MSC_K9G2G | | |
| | 2007-07-24 | 000BB3AD | | |
| | SM324BC | SM324BCISP_4K_SINGLE | | |
| | 2007-08-22 | 001C5A3D | | |
| | SM324BC | SM324BCISP_4K_TWIN | | |
| | 2007-08-22 | 001D4B7A | | |



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| 2007/10/12 | 2007-10-01 | 001D0F1A | V1.17.02 | 1. | MP tool support the SM325AB |
| | SM325AB | SM325AB | | 2. | MP tool add the SM325 wear-leveling 2 option, |
| | 2007-09-19 | 000A1026 | | | OPT1EWEARLEVELING=4 (ISP swap every 16000 times) and |
| | SM321BB | SM321BBISP | | | OPT1FWEARLEVELING=50 (block write count difference below |
| | 2007-09-19 | 000975CD | | | 50) |
| | SM321BB | SM321BBISP_MSC | | 3. | MP tool changed the SM324BC 2KB capacity |
| | 2007-09-19 | 000BF414 | | 4. | MP tool fixed the bad block counting for the 4KB and 2KB in |
| | SM321BB | SM321BBISP_2KMLC | | | pretest |
| | 2007-09-19 | 000C034F | | 5. | SM321BB ISP fixed the 2-lun application (read-only and mass |
| | SM321BB | SM321BBISP_2KMLC_MSC | | | storage) for the ready-only lun copy to the mass storage lun |
| | 2007-09-19 | 0018B45B | | 6. | SM321BB ISP fixed autorun didn't response in the special HP |
| | SM321BB | SM321BBISP_4K | | | platform with (0xA8/0xAA SCSI command) |
| | 2007-09-19 | 0019F01A | | 7. | SM324BC ISP fixed autorun in the Win2K |
| | SM321BB | SM321BBISP_2PLANE | | 8. | SM324BC ISP fixed 24 hours burn sometimes lost CID table in |
| | 2007-09-19 | 000BB373 | | | Micron Flash 2CE interleave mode |
| | SM321BB | SM321BBISP_2KMLC_MSC_K9G2G | | 9. | SM324BC ISP enable the Micron/Intel 72nm SLC Cache program |
| | 2007-10-11 | 000DA677 | | | to improve the performance |
| | SM324BC | SM324BCISP | | 10. | DBF disable the Micron MT29F32G08TAA copy back function |
| | | | | 11. | DBF add JS29F04G08AANC1, JS29F16G08AAMC1, |
| | | | | | JS29F32G08CAMC1, JS29F64G08FAMC1, K9G8G08U0A, |
| | | | | | HY27UF084G2B, HY27UT088G2A HY27UF08BGFB, |
| | | | | | HY27UH08AG5B HY27UF084G2B, HY27UG088G5B |
| 2007/10/31 | SM325AC | SM325ACISP | V1.17.05 | 1. | MP tool change the SM325AB/SM321CB to |



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| | 2007-10-24 SM324BC 2007-10-24 | 001E01C5 SM324BCISP 000DA67B | | <p>SM325AC/SM321CC</p> <ol style="list-style-type: none"> 2. MP tool support the SM325AC/SM321CC oscillator clock setting 3. SM321CC/SM325AC ISP support the USB-IF 4. SM324BC ISP corrected the 2KB/p and 4KB/p capacity inconsistency problem. 5. SM321CC/SM325 AC ISP lower the suspend current 6. SM325DBF file modify the clock and duty cycle 7. SM325DBF change max. clock to the "0B" |
| 2007/11/08 | SM325AC 2007-11-08 | SM325ACISP 0001E7A06 | V1.17.06 | <ol style="list-style-type: none"> 1. MP tool fixed the capacity will be half when the bad block equaled the max. bad block setting (example, SLC=20, MLC=44) 2. SM325 ISP fixed the 4CE Flash pretest failed 3. SM325 ISP support the read and write clock separated setting 4. SM325DBF change the clock setting format for the new MP tool/ISP 5. DBF added the Hynix HY27UF082G2B |
| 2007/11/28 | SM324BC 2007-11-27 SM324BC 2007-11-27 | SM324BCISP_4K_SINGLE 0001C5DA4 SM324BCISP_4K_TWIN 0001D4EE1 | V1.17.08 | <ol style="list-style-type: none"> 1. SM324BC 4KB ISP fixed USB can't recognized when 0x1F9 is bad block. 2. MP tool fixed the SM321CC/SM325AC "disk size" checking option and ISP checksum |



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| | | | | <ol style="list-style-type: none"> MP tool supported the Samsung K9LBG08U1M, and K9HCG08U5M DBF file Modify Intel JS29F04G08AANB1, JS29F08G08CANB1, JS29F08G08BANB1, JS29F16G08FANB1, JS29F08G08AAMC1, JS29F04G08AANC1 R/W 60Mhz DBF file of the SM325AC add PFC A1U8GA30GT DBF file of the SM321BB and SM324BC support the HY27UF082G2B with disable cache& copyback and 60Mhz DBF file add new K9LBG08U1M, K9HCG08U5M, MT29F2G08AAD, |
| 2007/12/15 | SM325AC 2007-12-14 SM324BC 2007-12-06 SM324BC 2007-12-06 | SM325ACISP 002FECF8 SM324BCISP_4K_SINGLE 0024ADCA SM324BCISP_4K_TWAIN 00236ECB | V1.17.12 | <ol style="list-style-type: none"> SM325AC ISP support the Micron/Intel/Toshiba 4KB MLC in Twin-2plane mode with clock 0x04 (48Mhz) SM321CC/SM325AC ISP fixed the nVidia GEFORCE 6100-405(MCP615) USB compatibility SM324BC 4KB ISP improve the small file wear-leveling SM324BC 4KB ISP fixed the USB disconnection randomly when ECC error handling MP tool fixed the SM321CC/SM325AC pretest in "erase bad block with 55 AA" function to mark the bad blocks MP tool format adjust the SM321CC/SM325AC FAT32/FAT16 alignment |



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| | | | | <ul style="list-style-type: none"> 7. MP tool fixed the turn on/off MU setting 8. MP tool support the DBF setting "@" for the single channel and "!" for the dual channel 9. MP tool fixed the "preload" function with the wrong file attribute after preload to UFD |
| 2007/12/19 | SM325AC 2007-12-19 SM321BB 2007-12-14 | SM325ACISP 002FEBE4 SM321BBISP_2PLANE 0001A53C6 | V1.17.13 | <ul style="list-style-type: none"> 1. SM325AC fixed LED lighting abnormal with setting after read/write randomly 2. SM325AC ISP lower suspend current by setting /WP to low when suspend mode 3. SM325 ISP improve inrush current by delaying the Flash access to 100ms when power-on 4. SM321BB ISP fixed the burn-in failed with Micron 29F088GMAA by doing the ISP wear-leveling 5. MP tool solve the format problem to cause the SM325 performance drop in G1215 6. MP tool fixed the BIOS choose UFD will frozen in Pheonx ROO7OJ4 7. SM325AC DBF correct the Micron/Intel 4KB MLC/SLC in Twin-2plane mode with clock 0x04 (48Mhz) 8. DBF file add ST NAND02GW3B2DN6, NAND08GW3C2AN6, NAND16GW3C2AN6, HYNIX HY27UF08BGFB, HY27UH08AG5B, MICRON MT29F8G08AAA, |



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| | | | | MT29F16G08DAA, MT29F32G08FAA |
| 2008/01/04 | SM325AC 2008-01-04 | SM325ACISP 002FF0A8 | V1.17.15 | <ol style="list-style-type: none"> 1. SM325AC ISP fixed the PC (ALI/SiS) restart and can't recognize UFD 2. SM325AC ISP fixed G1219 bug for the 4CE Flash 3. SM325AC ISP support PowerFlash 2-plane MLC 4. MP tool add SM321CC/SM325 downgrade flash supporting 5. MP tool add the SM321BB supporting Samsung K9LAG08U0A 6. MP tool fixed the check USB2.0/USB1.1 option error 7. MP tool add the capacity setting for data block size |
| 2008/01/09 | | | V1.17.17 | <ol style="list-style-type: none"> 1. MP tool correct the SM321CC/SM325AC for the Intel 50nm 4KB default bad block information position (4096) of the first page and last page 2. MP tool correct the SM321CC/SM325AC for the Micron 50nm default block information position (4096) of the first page |
| 2008/01/28 | | | V1.17.19 | <ol style="list-style-type: none"> 1. MP tool add the Hynix 57/60nm MLC for checking the last-2 page default bad block marking for the SM321BB, SM324BC, SM321CC, and SM325AC 2. MP tool changed the Intel 50nm MLC DBF file for the "disable cache" and lower clock to 60Mhz. |
| 2008/02/01 | | | V1.17.19 H0128 | <ol style="list-style-type: none"> 1. MP tool fixed the SM321CC/SM325 MP tool unstable (hang or over block setting) when using multiple devices in production. |



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| 2008/02/29 | SM324BC 2008-02-14 SM325AC 2008-02-26 | SM324BCISP_4K_SINGLE 001C9C28 SM325ACISP 002FFDEE | V1.17.20 H0227 | <ol style="list-style-type: none"> 1. SM324BC ISP fixed the Samsung 4KB MLC lost ISP when coping from 0x1F8 problem 2. SM325AC ISP fixed rebuild link table when ECC error to avoid the repeat marking same bad block 3. SM325AC ISP changes the "time to ready" < 100ms to meet the USB-IF spec. 4. MP tool add Vista FAT32 format to have better ready-boost performance to pass the premium logo. 5. DBF changed to enable the erase and programming status checking 6. Add DBF for the new Flash :HYNIX H27UAG8T2MTR, H27UCG8VFMTR, H27UDG8WFMTR, Micron MT29F8G08MAD |
| 2008/03/19 | SM325AC 2008-03-28 SM325AC 2008-03-03 SM325AC 2008-02-12 SM325AC 2008-02-12 | SM325ACISP 002F698A SM325ACISP-MI2P 002F60FC SM325ACISP-HYNIX-4K 003B3DDF SM325ACISP_AG-AND 001EDD7A | V1.17.24 H0328 | <ol style="list-style-type: none"> 1. SM325AC ISP dual channel to single plane for the Micron/Intel 50nm SLC/MLC in default setting and clock to 76Mhz 2. SM325ACISP-MI2P ISP for the dual channel/two plane of the Micron/Intel 50nm SLC/MLC for user setting 48Mhz 3. SM325AC ISP improve the random write performance 4. SM325AC ISP correct the USB-IF test vector pattern in set feature test package 5. SM325AC ISP support the Toshiba TH58NVG6D1DTG80 |



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| | | | | <ol style="list-style-type: none"> 6. SM325AC fixed the auto run in win2000 7. SM325ACISP-HYNIX-4K.BIN Beta ISP support the Hynix 48nm MLC dual channel 8. SM325ACISP_AG-AND.BIN Beta ISP support the AG-AND MLC 9. MP tool fixed the wear-leveling wrong counter setting for the ISP and data area :OPT1EWEARLEVELING=SM325 ISP WEARLEVELING and OPT1FWEARLEVELING=SM325 DATAWEARLEVELING 10. MP tool modify the dialog of the DBF options "automatic" change to "TSOP Dual Stacked Package", and "High Performance" change to "TSOP Standard Type". 11. MP tool fixed the LED blinking after Format in SM325AC |
| 2008-04-03 | SM325AC 2008-04-03 | SM325ACISP 002F5B5D | V1.17.24 H0328 | <ol style="list-style-type: none"> 1. Fixed the bug of the H038 for the rebuild the Fat in power cycling testing |
| 2008-04-14 | SM325AC 2008-04-11 | SM325ACISP 002C5E0E | V1.17.24 H0328 | <ol style="list-style-type: none"> 1. SM321CC/SM325AC fixed the USB bus reset when coping data 2. SM321CC/SM325AC move ISP to strong page of the MLC 3. SM325AC ISP enable the 4KB page twin-interleave mode 4. SM325ACC/SM325AC changes the "time to ready" 80ms to fixed the NEC PCI express card host time out problem in W2K 5. SM321CC/SM325AC ISP read count set to 10,000, which |



| | | | | SM325ISPWEARLEVELING=40 in the default.ini |
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| 2008-04-17 | SM324BC | SM324BCISP_4K_SINGLE | V1.17.24 | <ol style="list-style-type: none"> 1. SM324BC 4KB ISP fixed for the 4CE Flash 2. SM321BB ISP fixed the zip file with small files, which the power down didn't update the link table |
| | 2008-04-17 | 0024F456 | H0328 | |
| | SM324BC | SM324BCISP_4K_TWIN | | |
| | 2008-04-17 | 002386DC | | |
| | SM324BC | SM324BCPTEST_4K_Single | | |
| | 2008-04-17 | 00084682 | | |
| | SM324BC | SM324BCPTEST_4K_TWIN | | |
| | 2008-04-17 | 000 84D9C | | |
| | SM321BB | SM321BBISP_2plane | | |
| | 2008-04-16 | 001A56FA | | |
| 2008/05/23 | SM325AC | SM325ACISP-8CE | V1.17.28 | <ol style="list-style-type: none"> 1. SM321CC/SM325AC ISP fixed the wrong ECC marking address when ECC failed 2. MP tool add the option to choose the 4CE and 8CE supporting 3. MP tool support the SM325 64-pin for the 8CE pretest and ISP loading 4. SM325 8CE ISP for the SM325-64pin 8CE supporting 5. DBF file add HYNIX H27U2G8T2MTR,INTEL JS29F08G08CANC1 setting 6. DBF lower the clock for the Intel/Micron 50nm MLC read clock to 60Mhz(0x90) and write clock to 48Mhz.(0x80) 7. DBF correct the Samsung K9GAG08U0M and K9LBG08U1M |
| | 2008-05-21 | 0037972F | H0522 | |
| | SM325AC | SM325ACISP | | |
| | 2008-05-08 | 002C7CDC | | |



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| | | | | clock rate "0x0B" in twin mode |
| 2008/06/20 | SM325AC | SM325ACISP | V1.17.31 | 1. SM325AC ISP fixed the max. bad block setting > 52, which will |
| | 2008-06-17 | 002DD17F | H0620 | causes the write protected |
| | SM325AC | SM325ACISP-HYNIX-4K | | 2. SM325AC ISP fixed the Flash's write protection when VCC |
| | 2008-06-04 | 0033DAAF | | stay in the 2.7V to 1.8V range |
| | SM324BC | SM324BCISP_4K_SINGLE | | 3. SM325AC Hynix ISP for the Hynix 4KB/page Flash in twin |
| | 2008-06-16 | 002504D1 | | mode |
| | SM324BC | SM324BCISP_4K_TWIN | | 4. SM324BC ISP fixed WinCE 5.0 can't be recognized |
| | 2008-06-16 | 002391C8 | | 5. MP tool "extra setting" for the hidden area downloading |
| | | | | 6. Mp tool corrected the SM325AC "Twin" mode status in the |
| | | | | dialog |
| | | | | 7. MP tool "pretest" will use the DBF to enable/disable the |
| | | | | functions |
| | | | | 8. MP tool integrated the QC testing |
| | | | | 9. MP tool remove the setting option of the "4CE" and "8CE" to |
| | | | | avoid the wrong loading ISP. The SM325RX (8CE) will be |
| | | | | separated MP tool to avoid the wrong clock setting |
| | | | | 10. MP tool hide option Turn Off MU in dialog setting, and show it |
| | | | | by set in default.ini : [FUNCTION]ENABLETURNOFFMU=1. |
| | | | | 11. MP tool hide option Mix File in dialog setting, and show it by |
| | | | | set in default.ini : |
| | | | | [FUNCTION]EENABLESUPPORTMIXFILE=1. |
| | | | | 12. DBF file add ST NAND08GW3C2BN6,NAND16GW3C4BN6 |



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| | | | | 13. | DBF file enable the Hynix 48nm MLC program status checking |
| 2008/07/14 | SM325AC | SM325ACISP | V1.17.36 | 1. | SM325AC ISP fixed bad block marking (extra block) in dual-channel mode |
| | 2008-07-08 | 002DD8FC | H0714 | 2. | SM325ACISP-HYNIX separated ISP for the Hynix Flash |
| | SM325AC | SM325ACISP-HYNIX | | 3. | SM321CC/325AC ISP supports the Samsung K9F4G08U0B, K9K8G08U0B, K9WAG08U1B and K9GAG08U0D. |
| | 2008-07-02 | 00340DB3 | | 4. | SM3252A preliminary ISP |
| | SM3252A | SM3252AISP | | 5. | MP tool support the SM3251/3252 |
| | 2008-07-01 | 00315B86 | | 6. | MP tool fixed the auto run failed when enable FAT preload |
| | | | | 7. | MP tool Pretest mode add the "No erase all" option |
| | | | | 8. | Mp tool support Security + CDROM + Copytest + Preload |
| | | | | 9. | DBF file add the Samsung K9F4G08U0B, K9K8G08U0B, K9WAG08U1B, K9HBG08U1B, K9LAG08U0B and K9GAG08U0D |
| | | | | 10. | DBF file add the Intel/Micron add micron 34nm MT29F32G08MAA, MT29F64G08QAA, MT29F128G08TAA |
| | | | | 11. | DBF file enable the program status checking for the Hynix 48nm H27UBG8U5MTR, H27UCG8VFMTR, H27UCG8V5MTR SM321CC/SM325AC support Samsung MLC B-die: K9G8G08U0B and K9LAG08U0B |
| | | | | 12. | MP tool fixed the SM324BC QC add do-reset before read LBA 0 |



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| 2008/07/25 | SM325AC 2008-07-25 SM325AC 2008-07-25 SM3252AA 2008-07-25 | SM325ACISP 002EAF3 SM325ACISP-HYNIX 0034E9EF SM3252AISP 0034E9EF | V1.17.36 H0722 | <ol style="list-style-type: none"> 1. SM325 fixed the FAT rebuild to avoid the UFD can't recognized in the randomly power down-up testing 2. DBF added Spectek FNNL52A16K3WG, FNNL52A32K3W2, FNNL52A64K3W4 3. SM321CC/SM325AC DBF modify the Hynix 48nm MLC write clock from 90/90 to 07/01 in the H27UAG08U5MTR, H27UBG8U5MTR, H27UCG8VFMTR, H27UCG8V5MTR |
| 2008/08/28 | SM324BC 2008-08-26 SM325AC 2008-08-15 SM325AC 2008-08-21 | SM324BCISP_4K_SINGLE 0x258631 SM324BCISP_4K_TWIN 0x243003 SM325ACISP 0x2F13CF SM325ACISP-HYNIX-4K 0x348979 SM325ACptest-HYNIX-4K 0x8734B | V1.17.38 08/21 build | <ol style="list-style-type: none"> 1. SM325ACISP enhance randomly power down-up handling. 2. SM325ACISP Modify Check ISP1, ISP2 block in system init. 3. SM325ACISP Modify plug-in delay time from 95ms to 65ms to fixed Vista and Win2000 S3 and S4 issue. 4. SM325aclSP-Hynix enhance randomly power down-up handling. 5. SM325aclSP-Hynix Modify plug-in delay time from 95ms to 65ms to fixed Vista and Win2000 S3 and S4 issue. 6. SM324BCISP_4K_SINGLE and SM324BCISP_4K_TWIN Modify Check ISP1~3, refresh ISP when any one ISP has ECC or no complete ISP code, for reduce write endurance. If all ISP fail, jump to ROM code. 7. MP tool Modify Driving Current set to 8 mA with Hynix, Intel and Micron Flash 8. MP tool Modify Hynix 4K MLC Flash use |



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| | | | | <p>SM325ACISP-HYNIX-4K.BIN, SM325ACptest-HYNIX-4K.BIN, otherwise use normal ISP and Pretest file.</p> <p>9. SM321CC/SM325AC DBF modify write clock from 0B/0B to 90/90 and Disable Cache Program in the K9GAG08U0D.</p> <p>10. SM321CC/SM325AC DBF modify write clock from 90/90 to 07/07 and enable Program Status Check in the H27UAG08U5MTR. Modify write clock from 90/90 to 07/01 and enable Program Status Check in H27UBG8U5MTR, H27UCG8VFMTR.</p> <p>11. MP tool fixed SM325AC/SM321CC download ISP with non-useful data to avoid checksum error.</p> <p>12. MP tool add button to dump ISP and dump block data for SM325AC/SM321CC.</p> <p>13. MP tool add function disable start button if run autoloader without ini file.</p> <p>14. MP tool add function setup mp version with file setting. set.</p> |
| 2008/09/18 | SM325AC 2008-09-10 | SM325ACISP 0x38E86E | V1.17.39 09/18 build | <p>1. Fix MP preload FAT warning as last access time error.</p> <p>2. SM325ACISP Modify rebuilds table flow and adds check 2.7V action before program each time to enhance Power cycling handling.</p> <p>3. SM325ACISP Modify plug-in delay time from 95ms to 65ms and</p> |



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| | | | | after pull high write protect, then set VDT reset register to fixed S3,S4 issue in Vista and Windows2000. |
| 2008/10/29 | SM325AC 2008-10-06 SM325AC 2008-10-17 SM3252AC 2008-10-23 | SM325ACISP 0x39EB72 SM325ACISP-InterLeave 0x39F602 SM3252ACISP 0x32A0E6 | V1.17.42 10/29 build | <ol style="list-style-type: none"> 1. SM325AC ISP Modify rebuild table Flow, save link table Flow and check temp block flow to enhance power cycling. 2. SM325ACISP-InterLeave support K9LBG08U0M, K9KAG08U0M, K9LAG08U0M, K9L8G08U0A, K9K8G08U0B and K9K8G08U0A in one channel. 3. SM3252ACISP Modify plug-in delay time from 95ms to 65ms and after pull high write protect, then set VDT reset register to fixed S3,S4 issue in Vista and Windows2000. 4. SM3252ACISP add info block backup. 5. DBF file add Samsung K9F2G08U0B. 6. MP tool add NTFS Format. 7. MP tool add QC test support MultiLun. |
| 2008/11/14 | SM325AC 2008-10-06 SM325AC 2008-10-17 SM3252AC 2008-11-11 | SM325ACISP 0x39EB72 SM325ACISP-InterLeave 0x39F602 SM3252ACISP 0x32A2A4 | V1.17.42 11/10 build | <ol style="list-style-type: none"> 1. SM3252ACISP enable watch dog function. |
| 2008/12/26 | SM325AC 2008-12-19 | SM325ACISP 0x3a0a5c | V1.17.44 12/18 build | <ol style="list-style-type: none"> 1. SM325ACISP reduces time when build table to fixed timeout issue in power book Mac G4 (10.4.10) and PS3 (F/W: 2.5). |



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| | SM325AC 2008-12-26 | SM325ACISP-InterLeave 0x3A2B19 | | 2. SM325ACISP modify auto copy back issue in 2K/4K SLC flash to fixed data loss in Normal power cycling test. |
| | SM3252AC 2008-12-19 | SM3252ACISP 0x7e677 | | 3. SM325ACISP-InterLeave modify auto copy back issue in 2K/4K SLC flash to fixed data loss in Normal power cycling test. |
| | SM3252BA 2008-12-19 | SM3252BAISP 0x32c7cd | | 4. SM3252ACISP and SM3252BAISP modify auto copy back issue in 2K/4K SLC flash to fixed data loss in Normal power cycling test. |
| | | | | 5. SM3252BAISP first release. |
| | | | | 6. MP Tool support SM3252BA and SM3252BA Force ID function. |
| | | | | 7. DBF file add Micron 34nm flash MT29F64G08CFAAA DDP and MT29F128G08TAA QDP. |
| | | | | 8. DBF file add flash ID to support K9GAG08U0Dx2 bonding 1CE pin. |
| | | | | 9. DBF file add Hynix 41nm H27UBG8T2MYR ULGA flash. |
| 2009/01/12 | SM325AC 2008-12-29 | SM325ACISP 0x397E78 | V1.17.46 01/12 build | 1. SM325ACISP modify reserve block process flow to fix over spare pool bug. |
| | SM3252AC 2009-01-08 | SM3252ACISP 0x33022F | | 2. SM3252ACISP and SM3252ACISP_MI_34nm modify Error handling flow to fixed Micron 34nm QDP and DDP burn-in fail issue |
| | SM3252AC 2009-01-08 | SM3252ACISP_MI_34nm 0x333769 | | 3. SM3252ACISP_MI_34nm enable random in/out command to support twin+2plane mode in Micron 34nm |
| | SM3252BA 2009-01-08 | SM3252BAISP 0x33022F | | 4. SM3252BAISP and SM3252BAISP_MI_34nm modify Error |



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| | SM3252BA 2009-01-08 | SM3252BAISP_MI_34nm 0x333769 | | <p>handling flow to fixed Micron 34nm QDP and DDP burn-in fail issue</p> <ol style="list-style-type: none"> SM3252BAISP_MI_34nm enable random in/out command to support twin+2plane mode in Micron 34nm DBF file add Hynix 48nm H27U8G8T2B MP tool modify display mistake in twin mode |
| 2009/02/03 | SM325AC 2009-02-02 SM3252AC 2009-02-02 SM3252AC 2009-02-03 SM3252BA 2009-02-02 SM3252BA 2009-02-03 | SM325ACISP 0x39A94A SM3252ACISP 0x334C45 SM3252ACISP_MI_34nm 0x32CB25 SM3252BAISP 0x334C45 SM3252BAISP_MI_34nm 0x32CB25 | V1.17.47 02/09 build | <ol style="list-style-type: none"> SM325AC change 2-Plane Program Page CMD from 80h-11h-81h-10h to 80h-11h-80h-10h in Intel/Micron 50nm flash. SM325AC change 2-Plane Program Page for Internal Data Move CMD from 85h-11h-81h-10h to 85h-11h-85h-10h in Intel/Micron 50nm flash. SM3252ACISP and SM3252ACISP_MI_34nm change 2-Plane Program Page CMD from 80h-11h-81h-10h to 80h-11h-80h-10h in Intel/Micron 34nm. SM3252ACISP and SM3252ACISP_MI_34nm change 2-Plane Program Page for Internal Data Move CMD from 85h-11h-81h-10h to 85h-11h-85h-10h in Intel/Micron 34nm. SM3252BAISP and SM3252BAISP_MI_34nm change 2-Plane Program Page CMD from 80h-11h-81h-10h to 80h-11h-80h-10h in Intel/Micron 34nm. SM3252BAISP and SM3252BAISP_MI_34nm change 2-Plane Program Page for Internal Data Move CMD from |



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| | | | | <p>85h-11h-81h-10h to 85h-11h-85h-10h in Intel/Micron 34nm.</p> <p>7. SM3252ACISP_MI_34nm and SM3252BAISP_MI_34nm add program status check and host retry.</p> <p>8. DBF file add Samsung K9F1G08U0C.</p> |
| 2009/02/12 | <p>SM325AC</p> <p>2009-02-11</p> <p>SM325AC</p> <p>2009-02-11</p> <p>SM325AC</p> <p>2009-02-11</p> <p>SM325AC</p> <p>2009-02-11</p> <p>SM3252AC</p> <p>2009-02-12</p> <p>SM3252AC</p> <p>2009-02-12</p> <p>SM3252BA</p> <p>2009-02-12</p> <p>SM3252BA</p> <p>2009-02-12</p> | <p>SM325ACISP</p> <p>0x39A002</p> <p>SM325ACISP-HYNIX</p> <p>0x34049C</p> <p>SM325ACISP_Disable_Interleave</p> <p>0x399399</p> <p>SM325ACISP-HYNIX_Disable_Interleave</p> <p>0X341F75</p> <p>SM3252ACISP</p> <p>0x337093</p> <p>SM3252ACISP_MI_34nm</p> <p>0x32E170</p> <p>SM3252BAISP</p> <p>0x337093</p> <p>SM3252BAISP_MI_34nm</p> <p>0x32E170</p> | <p>V1.17.48</p> <p>02/16 build</p> | <p>1. SM325ACISP modify change mode to support Samsung 42nm K9LBG08U0D and K9HCG08U1D flash.</p> <p>2. SM325ACISP-HYNIX modifies build table flow to reduce device Initial time.</p> <p>3. SM325ACISP_Disable_Interleave first release</p> <p>4. SM325ACISP-HYNIX_Disable_Interleave first release</p> <p>5. SM3252ACISP and SM3252BAISP modify change mode to support Samsung 42nm K9LBG08U0D and K9HCG08U1D flash.</p> <p>6. SM3252ACISP_MI_34nm and SM3252BAISP_MI_34nm modify build table flow to reduce device Initial time.</p> <p>7. MP tool add Disable Interleave option when add [OPTION] ShowDisableInterleave=1 in default.ini</p> <p>8. MP tool fixed the CDROM size larger than 4.0GB issue result public size mistake.</p> |



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| 2009/03/02 | SM325AC | SM325ACISP | V1.17.50 | <ol style="list-style-type: none"> 1. SM325ACISP modify change mode to support ST NAND08GW3F2AN6. 2. SM3252ACISP and SM3252BAISP modify change mode to support ST NAND08GW3F2AN6. 3. SM325ACISP and SM325ACISP-HYNIX modify to fixed resume fail in Mac (10.4.8) PC. 4. SM3252ACISP and SM3252BA ISP modify to fixed resume fail in Mac (10.4.8) PC. 5. SM3252ACISP_MI_34NM and SM3252BAISP_MI_34NM modify to fixed resume fail in Mac (10.4.8) PC. 6. DBF file add ST NAND08GW3F2AN6, NAND16GW3D2AN6 and NAND04GW3C2BN6 7. MP tool support Auto- start production with factory driver. |
| | 2009-02-25 | 0x39B446 | 03/05 build | |
| | SM325AC | SM325ACISP-HYNIX | | |
| | 2009-03-02 | 0x341BEB | | |
| | SM3252AC | SM3252ACISP | | |
| | 2009-03-02 | 0x3378DD | | |
| | SM3252AC | SM3252ACISP_MI_34NM | | |
| | 2009-03-02 | 0x32DDF6 | | |
| | SM3252BA | SM3252BAISP | | |
| | 2009-03-02 | 0x3378DD | | |
| | SM3252BA | SM3252BAISP_MI_34NM | | |
| | 2009-03-02 | 0x32DDF6 | | |
| 2009/03/12 | SM325AC | SM325ACISP-HYNIX | V1.17.52 | <ol style="list-style-type: none"> 1. SM325ACISP-Hynix modify to support ST flash NAND08GW3F2AN6, NAND16GW3D2AN6 and NAND04GW3C2BN6 in twin+2plane 2. SM3252ACISP and SM3252BAISP modify to support ST flash NAND08GW3F2AN6, NAND16GW3D2AN6 and NAND04GW3C2BN6 in twin+2plane 3. SM3252ACISP and SM3252BAISP modify to support Hynix 41nm flash H27UBG8T2MYR in twin+2plane 4. SM3252BBISP first formal release. |
| | 2009-03-11 | 0x341DF7 | 03/19 build | |
| | SM3252AC | SM3252ACISP | | |
| | 2009-03-11 | 0x3384C6 | | |
| | SM3252BA | SM3252BAISP | | |
| | 2009-03-11 | 0x3384C6 | | |
| | SM3252BB | SM3252BBISP | | |
| | 2009-03-11 | 0X31E127 | | |
| | SM3252BB | SM3252BAISP_MI_34NM | | |



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| | 2009-03-11 | 0X31E127 | | <ol style="list-style-type: none"> 5. SM3252BBISP support Micron L62A MT29F16G08CBABA and Micron L63B MT29F32G08CBABA. 6. MP tool modify to support SM3252BB. |
| 2009/04/14 | SM325AC 2009-04-01 SM3252AC 2009-04-14 SM3252AC 2009-04-14 SM3252BA 2009-04-14 SM3252BA 2009-04-14 SM3252BB 2009-04-14 SM3252BB 2009-04-14 | SM325ACISP 0x39B6C0 SM3252ACISP 0x338CD0 SM3252ACISP_MI_34NM 0x32F841 SM3252BAISP 0x338CD0 SM3252BAISP_MI_34NM 0x32F841 SM3252BBISP 0x3204E0 SM3252BBISP_MI_34NM 0x32194B | V1.17.54 V2 04/09 build | <ol style="list-style-type: none"> 1. SM325ACISP response Host setup command before build MU to fixed PS3 timeout issue. 2. SM3252ACISP and SM3252ACISP_MI_34NM response host setup command before build MU to reduce Initial time 3. SM3252BAISP and SM3252BAISP_MI_34NM response host setup command before build MU to reduce Initial time 4. SM3252BBISP and SM3252BBISP_MI_34NM response host setup command before build MU to reduce Initial time 5. SM3252ACISP_MI_34NM support two plane read to improve Read performance in Micron/Intel L63A. 6. SM3252BAISP_MI_34NM support two plane read to improve Read performance in Micron/Intel L63A. 7. SM3252BBISP modify change mode to support Samsung 42nm D Die flash. 8. SM3252BBISP_MI_34NM modify support L62A, L63B and L63A in twin, 2plane mode. 9. MP tool add force ID to support Micron L63A 128Gb in SM3252BB 10. MP tool add Eject button in Windows 2000. |



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| | | | | 11. MP tool fixed Config Hub error in PCI to USB port. |
| 2009/05/26 | SM325AC | SM325ACISP | V1.17.60 V1 | <ol style="list-style-type: none"> 1. SM325ACISP modifies the USB device read only when bad block increase over our bad block threshold. 2. SM325ACISP-HYNIX modifies the USB device read only when bad block increase over our bad block threshold. 3. SM3252ACISP, SM3252BAISP and SM3252BBISP modify the USB device read only when bad block increase over our bad block threshold. 4. SM3252ACISP_MI_34NM, SM3252BAISP_MI_34NM and SM3252BBISP_MI_34NM modify the USB device read only when bad block increase over our bad block threshold. 5. SM3252ACISP_MI_34NM, SM3252BAISP_MI_34NM and SM3252BBISP_MI_34NM modify the USB device read only when bad block increase over our bad block threshold. 6. SM3252ACISP_MI_34NM, SM3252BAISP_MI_34NM and SM3252BBISP_MI_34NM change card mode from Twin,2plane to Twin,1plane when original bad block too much, it can improve yield rate in Micron/Intel L63A when initial card 7. SM3252BAISP support Hynix 41nm 32Gbit SDP, DDP and QDP. 8. SM3252BBISP support Hynix 41nm 32Gbit SDP, DDP and QDP. 9. SM3252BBISP_MI_34NM support Micron/Intel L63B flash in Twin, 1plane mode. |
| | 2009-05-26 | 0x39DC66 | 05/26 build | |
| | SM325AC | SM325ACISP-HYNIX | | |
| | 2009-05-26 | 0x33E1AE | | |
| | SM3252AC | SM3252ACISP | | |
| | 2009-05-26 | 0x33A46E | | |
| | SM3252AC | SM3252ACISP_MI_34NM | | |
| | 2009-05-26 | 0x32FB8D | | |
| | SM3252BA | SM3252BAISP | | |
| | 2009-05-26 | 0x33A46E | | |
| | SM3252BA | SM3252BAISP_MI_34NM | | |
| | 2009-05-26 | 0x32FB8D | | |
| | SM3252BB | SM3252BBISP | | |
| | 2009-05-26 | 0x321797 | | |
| | SM3252BB | SM3252BBISP_MI_34NM | | |
| | 2009-05-26 | 0x322660 | | |



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| | | | | 10. MP tool added SM3252B Pretest/ISP bin file selection. |
| 2009/06/04 | SM325AC | SM325ACISP | V1.17.61 V4 | <ol style="list-style-type: none"> 1. SM325ACISP and SM325ACISP-HYNIX support the maximum capacity of CDROM is 2GB. 2. SM3252ACISP and SM3252ACISP_MI_34NM support the maximum capacity of CDROM is 2GB. 3. SM3252BAISP and SM3252BAISP_MI_34NM support the maximum capacity of CDROM is 2GB. 4. SM3252BBISP and SM3252BBISP_MI_34NM support the maximum capacity of CDROM is 2GB. 5. SM3252BAISP and SM3252BAISP_MI_34NM fixed recognize fail when plug in/out multiple device with Hub in production line. 6. SM3252BBISP and SM3252BBISP_MI_34NM fixed recognize fail when plug in/out multiple device with Hub in production line. 7. SM3252BAISP and SM3252BAISP_MI_34NM support over 32 GB capacities with multi-Lun. 8. MP tool fixed copy compare fail with enable CDROM. 9. MP tool fixed TCP/IP Handler controller always disconnects issue after autodownload INI. 10. MP tool add scan USB hot key with "F-5". 11. MP tool add start testing hot key with "Space Key". 12. MP tool fixed copy compare less than real percentage during |
| | 2009-06-04 | 0x3A2010 | 06/10 build | |
| | SM325AC | SM325ACISP-HYNIX | | |
| | 2009-06-04 | 0x343CFB | | |
| | SM3252AC | SM3252ACISP | | |
| | 2009-06-04 | 0x36D9B7 | | |
| | SM3252AC | SM3252ACISP_MI_34NM | | |
| | 2009-06-04 | 0x35F76A | | |
| | SM3252BA | SM3252BAISP | | |
| | 2009-06-04 | 0x36D9B7 | | |
| | SM3252BA | SM3252BAISP_MI_34NM | | |
| | 2009-06-04 | 0x35F76A | | |
| | SM3252BB | SM3252BBISP | | |
| | 2009-06-04 | 0x3213E2 | | |
| | SM3252BB | SM3252BBISP_MI_34NM | | |
| | 2009-06-04 | 0x322177 | | |



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| | | | | enable CDROM+ Reserve Area Only. 13. MP tool support copy compare with mass storage driver. |
| 2009/06/26 | SM3252AC | SM3252ACISP | V1.17.64 V1 | 1. SM3252BBISP support Micron SLC M62B MT29F16G08ABABA |
| | 2009-06-26 | 0x3907DC | 06/26 build | 2. SM3252BBISP support Intel L62A JS29F16G08AAMD2. |
| | SM3252AC | SM3252ACISP_MI_34NM | | 3. SM3252BBISP adjust setup time of ALE, hold time of ALE, setup time of DATA and hold time of DATA to fixed Hynix 41nm |
| | 2009-06-23 | 0x35FCC4 | | USB-disconnection when data read. |
| | SM3252BA | SM3252BAISP | | 4. SM3252BBISP fixed MT29F128G08CJAAx4 make Auto Run fail |
| | 2009-06-26 | 0x3907DC | | 5. SM3252BAISP fixed MT29F128G08CJAAx4 make Auto Run fail |
| | SM3252BA | SM3252BAISP_MI_34NM | | 6. SM3252BAISP adjust setup time of ALE, hold time of ALE, setup time of DATA and hold time of DATA to fixed Hynix 41nm |
| | 2009-06-23 | 0x35FCC4 | | USB-disconnection in ATTO testing when data read. |
| | SM3252BB | SM3252BBISP | | 7. MP tool added the format checking during each write command to |
| | 2009-06-26 | 0x36EB67 | | Screen zero capacity |
| | SM3252BB | SM3252BBISP_MI_34NM | | 8. MP tool adjust MAXDATABLK from 1000 to 1008. |
| | 2009-06-26 | 0x3569D3 | | |